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Qualified for Automotive Applications

Typical V_{OLP} (Output Ground Bounce)
 <0.8 V at V_{CC} = 3.3 V, T_A = 25°C

- Typical V_{OHV} (Output V_{OH} Undershoot)
 >2.3 V at V_{CC} = 3.3 V, T_A = 25°C
- Supports Mixed-Mode Voltage Operation on All Ports
- I_{off} Supports Partial-Power-Down Mode Operation
- ESD Protection Exceeds JESD 22
 - 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

vv			

description/ordering information

This hex Schmitt-trigger inverter is designed for 2-V to 5.5-V V_{CC} operation.

The SN74LV14A contains six independent inverters. This device performs the Boolean function $Y = \overline{A}$.

This device is fully specified for partial-power-down applications using I_{off}. The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

ORDERING INFORMATION[†]

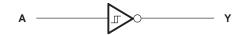
T _A	PACKA	AGE‡	ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 105°C	TSSOP – PW	Tape and reel	SN74LV14ATPWRQ1	LV14ATQ

[†] For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at http://www.ti.com.

[‡]Package drawings, thermal data, and symbolization are available at http://www.ti.com/packaging.

FUNCTION TABLE (each inverter)						
INPUT A	OUTPUT Y					
Н	L					
L	Н					

logic diagram, each inverter (positive logic)



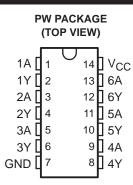


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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC} Input voltage range, V_I (see Note 1)	
Voltage range applied to any output in the high-impedance or power-off state, V _O (see Note 1)	-0.5 V to 7 V
Output voltage range, V_O (see Notes 1 and 2)	
Input clamp current, I_{IK} ($V_I < 0$)	
Output clamp current, I _{OK} (V _O < 0)	–50 mA
Continuous output current, $I_O (V_O = 0 \text{ to } V_{CC})$	±25 mA
Continuous current through V _{CC} or GND	
Package thermal impedance, θ_{JA} (see Note 3)	
Storage temperature range, T _{stg}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

2. This value is limited to 5.5 V maximum.

3. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 4)

			MIN	MAX	UNIT	
VCC	Supply voltage		2	5.5	V	
		$V_{CC} = 2 V$	1.5			
	1 Path Land Second configure	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	$V_{CC} \times 0.7$			
VIH	High-level input voltage	$V_{CC} = 3 V \text{ to } 3.6 V$	$V_{CC} \times 0.7$		V	
		V _{CC} = 4.5 V to 5.5 V	$V_{CC} \times 0.7$			
		$V_{CC} = 2 V$		0.5		
VIL	Level level l'anot colle en	V_{CC} = 2.3 V to 2.7 V		$V_{CC} \times 0.3$		
	Low-level input voltage	$V_{CC} = 3 \vee to 3.6 \vee$		$V_{CC} \times 0.3$	V	
		V _{CC} = 4.5 V to 5.5 V		$V_{CC} \times 0.3$		
VI	Input voltage		0	5.5	V	
VO	Output voltage		0	VCC	V	
		$V_{CC} = 2 V$		-50	μA	
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		-2	mA	
ЮН	High-level output current	$V_{CC} = 3 \vee to 3.6 \vee$		-6		
		V _{CC} = 4.5 V to 5.5 V		-12		
		$V_{CC} = 2 V$		50	μA	
	Level and a devidence of	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		2		
IOL	Low-level output current	$V_{CC} = 3 V \text{ to } 3.6 V$		6	mA	
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$		12		
TA	Operating free-air temperature		-40	105	°C	

NOTE 4: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.



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PARAMETER	TEST CONDITIONS		Vcc	MIN	TYP	MAX	UNIT			
V _{T+}			2.5 V			1.75				
Positive-going			3.3 V			2.31	V			
threshold			5 V			3.5				
V _{T-}			2.5 V	0.75						
Negative-going			3.3 V	0.99			V			
threshold			5 V	1.5						
			2.5 V	0.25		1				
∆V _T Hysteresis (V _{T+} – V _{T−})			3.3 V	0.33		1.32	V			
			5 V	0.5		2				
	I _{OH} = -50 μA		2 V to 5.5 V	V _{CC} -0.1						
N/	$I_{OH} = -2 \text{ mA}$		2.3 V	2			V			
V _{OH}	$I_{OH} = -6 \text{ mA}$		3 V	2.48			v			
	$I_{OH} = -12 \text{ mA}$		4.5 V	3.8			1			
	I _{OL} = 50 μA		2 V to 5.5 V			0.1				
Max	I _{OL} = 2 mA		2.3 V			0.4	V			
V _{OL}	I _{OL} = 6 mA		3 V			0.44	v			
	I _{OL} = 12 mA		4.5 V			0.55				
Ц	$V_I = V_{CC}$ or GND		0 V to 5.5 V			±1	μΑ			
ICC	$V_I = V_{CC}$ or GND,	$I_{O} = 0$	5.5 V			20	μΑ			
l _{off}	V_I or $V_O = 0$ to 5.5 V		0 V			5	μΑ			
0			3.3 V		2.3					
Ci	$V_I = V_{CC}$ or GND		5 V		2.3		pF			

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

switching characteristics over recommended operating free-air temperature range, V_{CC} = 3.3 V \pm 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	то	LOAD	Τį	λ = 25°C	;	MIN		
PARAMETER	(INPUT)	(OUTPUT)	CAPACITANCE	MIN	TYP	MAX	IVIIIN	MAX	UNIT
^t pd	А	Y	C _L = 50 pF		9.6	16.3	1	20.4	ns

switching characteristics over recommended operating free-air temperature range, V_{CC} = 5 V \pm 0.5 V (unless otherwise noted) (see Figure 1)

DADAMETER	FROM	то	LOAD	Τį	λ = 25°C	;		MAX	
PARAMETER	(INPUT)	(OUTPUT)	CAPACITANCE	MIN	TYP	MAX	MIN	MAX	UNIT
^t pd	А	Y	C _L = 50 pF		6.7	10.6	1	14	ns



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noise characteristics, V_{CC} = 3.3 V, C_L = 50 pF, T_A = 25°C (see Note 5)

	PARAMETER	MIN	TYP	MAX	UNIT
V _{OL(P)}	Quiet output, maximum dynamic V _{OL}		0.2	0.8	V
V _{OL(V)}	Quiet output, minimum dynamic V _{OL}		-0.1	-0.8	V
V _{OH(V)}	Quiet output, minimum dynamic V _{OH}		3.1		V
VIH(D)	High-level dynamic input voltage	2.31			V
V _{IL(D)}	Low-level dynamic input voltage			0.99	V

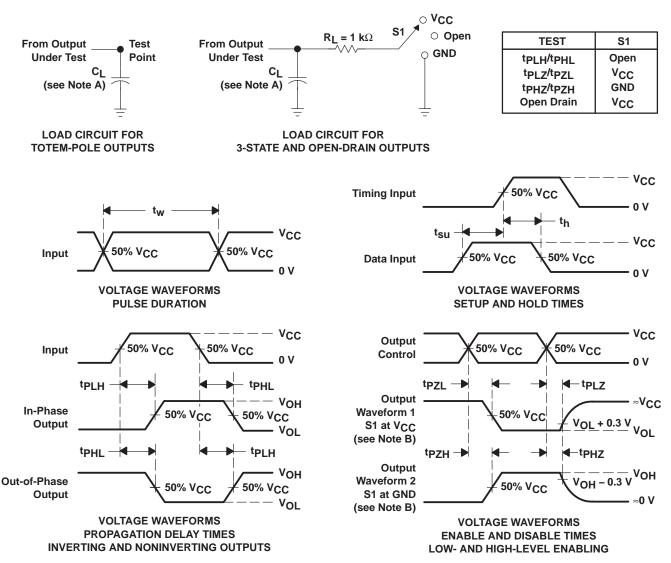
NOTE 5: Characteristics are for surface-mount packages only.

operating characteristics, $T_A = 25^{\circ}C$

PARAMETER		TEST CONDITIONS		V _{CC}	TYP	UNIT
<u> </u>	Dever dissinction conscitutes	C. 50 pF	£ 10 MU-	3.3 V	8.8	۶F
Cpd	Power dissipation capacitance	C _L = 50 pF,	f = 10 MHz	5 V	9.6	рг



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PARAMETER MEASUREMENT INFORMATION

NOTES: A. C₁ includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, Z_O = 50 Ω , t_r \leq 3 ns, t_f \leq 3 ns.
- D. The outputs are measured one at a time, with one input transition per measurement.
- E. tp_{I 7} and tp_{H7} are the same as t_{dis} .
- F. t_{PTI} and t_{PTH} are the same as t_{en} .
- G. t_{PLL} and t_{PLH} are the same as t_{pd} .
- H. All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms





PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins I	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN74LV14ATPWRG4Q1	ACTIVE	TSSOP	PW	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LV14ATPWRQ1	ACTIVE	TSSOP	PW	14	2000	Pb-Free (RoHS)	CU NIPDAU	Level-1-250C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. **TBD:** The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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OTHER QUALIFIED VERSIONS OF SN74LV14A-Q1 :

- Catalog: SN74LV14A
- Enhanced Product: SN74LV14A-EP

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Enhanced Product Supports Defense, Aerospace and Medical Applications

MECHANICAL DATA

MTSS001C - JANUARY 1995 - REVISED FEBRUARY 1999

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- D. Falls within JEDEC MO-153



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